

Embedded Machine Learning

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February 27, 2018

Topics

- MicroPython
- Thonny
- TinyML
- Tensorflow Lite
- Microsoft Azure Sphere

MicroPython

- MicroPython is a software implementation of the Python programming language designed for microcontrollers and embedded systems.
- It is a lean and efficient version of Python that can run on devices with limited resources such as low-power microcontrollers, development boards, and other small computing devices.

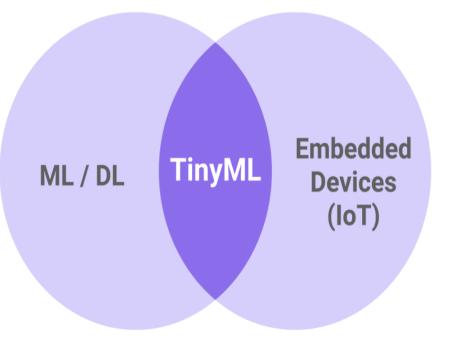
- MicroPython supports a wide range of microcontrollers, including popular platforms like Arduino, ESP32, ESP8266, Raspberry Pi Pico, and many more.
- It provides access to hardware peripherals and I/O pins, allowing developers to control sensors, actuators, and other components connected to the microcontroller.

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TinyML

- TinyML is a type of machine learning that allows models to run on smaller, less powerful devices.
- It involves hardware, algorithms, and software that can analyze sensor data on these devices with very low power consumption, making it ideal for always-on usecases and battery-operated devices.



Why TinyML

- **Bandwidth** As an example, a device at 100Hz sampling rate produces 360,000 data points each hour. Now imagine the amount of data produced by a fleet of these devices.
- **Latency** "time between when a system takes in a sensory input and responds to it". In case of conventional ML deployment data must be first sent to an ML application. This increases the time in which an edge device can take action as it waits for the response.
- **Economics** Cloud is cheap but not so cheap. It still costs money to ingest large amounts of data, especially if it must happen in real-time.
- **Reliability** In case of high-frequency sampling, it might be hard to ensure that data arrives to a target in the same order as it was produced by an edge device.
- **Privacy** TinyML processes data on-device and is not sent through network. This reduces the surface for data abuse.

Assignment 2

- For the semester Complex Engineering Problem.
- Submit Front-end, Data, Software and Hardware Architectures. Along with short description of each architecture.